2024 NSTC SYMPOSIUM

Monday, October 28, 2024



7:00-8:30 AM	EVENT CHECK-IN		
8:30-8:40 AM	WELCOMING REMARKS	Regency Ballroom	
8:40-9:05 AM	Keynote Presentation	Regency Ballroom	
	Recorded Remarks: The Honorable U.S. Secretary of Commerce Gina Raimondo		
	Remarks: Dr. Laurie Locascio, Under Secretary of Commerce for Standards and Technology and Director of the National Institute of Standards and Technology (NIST)		
9:05-9:30 AM	NSTC OVERVIEW	Regency Ballroom	
	Deirdre Hanford, CEO, Natcast		
9:30-10:00 AM	Uniting Government Efforts to Ensure Semiconductor Leadership	Regency Ballroom	
	Deirdre Hanford (Moderator), CEO, Natcast		
	Dr. David A. Honey, Deputy Under Secretary of Defense for Resea Engineering	rch and	
	Dr. Jay Lewis, Director, CHIPS, NSTC Program, Department of Com	nmerce	
	Helena Fu, Director, Department of Energy, Office of Critical and Technologies	Emerging	
	Dr. Susan Margulies, Assistant Director, NSF, Directorate for Engi	neering	
10:00-10:20 AM	Вкеак		
10:20-10:45 AM	Keynote: Collaborative Innovation: Semiconductors' Role in Solving Global Challenges	Regency Ballroom	
	Mark Papermaster, CTO and Executive Vice President, AMD		
10:45-11:00 AM	NSTC RESEARCH AND DEVELOPMENT OVERVIEW	Regency Ballroom	
	Dr. Robert Chau, Sr. Vice President of Research, Natcast		
11:00-11:40 AM	R&D TOPIC 1: THE SEMICONDUCTOR R&D LANDSCAPE: NEEDS AND OPPORTUNITIES	Regency Ballroom	
	Bill Chappell (Moderator), Vice President and CTO, Strategic Missions and Technologies Division, Microsoft <i>Vikas Chandra, Senior Director, Meta Reality Labs</i>		
	Gordon Keeler, CEO and co-founder, Beacon Photonics		
	Sayeef Salahuddin, TSMC Distinguished professor of Electrical Engineering and Computer Sciences, University of California Berkeley Mark Webb, Principal Analyst and Advisor, MKW Ventures Consulting, LLC		

11:40-12:05 AM	Keynote: Bridging Fundamental Research to Industrial Innovation: America's Unique Advantage	Regency Ballroom
	Dr. Naveen Verma, Professor, Director of Keller Center for Innovation in Engineering Education, Princeton University, and CEO and Co-founder, EnCharge AI	
12:05-1:15 PM	LUNCH	Regency Ballroom
1:15-1:55 PM	R&D TOPIC 2: SHAPING THE FUTURE OF HEALTH SCIENCES THROUGH INNOVATIONS IN SEMICONDUCTOR AND EDGE AI	Regency Ballroom
	Dr. Ahmad Bahai (Moderator), CTO, Texas Instruments Max Shulaker, Senior Director Biomedical Engineering, Analog Devices Dr. Dina Katabi, Professor, MIT and Co-founder, Emerald Innovations Gregory Kovacs, Biomedical Innovator and Professor, Stanford University Paul Gerrish, VP of Technology, Advanced Product Development, Medtronic	
1:55-2:25 PM	R&D TOPIC 3: SEMICONDUCTORS FUELING THE DISRUPTION IN THE AUTOMOTIVE INDUSTRY	Regency Ballroom
	Vidya Rajagopalan, Senior VP Electrical Hardware, Rivian	
2:25-2:55 PM	OVERVIEW OF THE NSTC WORKFORCE CENTER OF EXCELLENCE	Regency Ballroom
	John Ratliff, Executive Director, Workforce Center of Excellence Deirdre Hanford, CEO, Natcast	
2:55-3:15 PM	Впеак	Regency Ballroom
3:15-4:00 PM	STATE OF THE U.S. SEMICONDUCTOR WORKFORCE	Regency Ballroom
	Richard-Duane Chambers (Moderator), Director, Policy and Integration, NIST CHIPS R&D Office Pearl Esau, Founder, Shan Strategies Rachel Lipson, Senior Policy Advisor, CHIPS for America Behzad Razavi, Professor, Electrical and Computer Engineering, UCLA Jared Zerbe, Director of Engineering, Apple	
4:00-5:20 PM	STARTUPS TAKE THE STAGE: PITCH COMPETITION	Regency Ballroom
5:20-5:25 PM	CLOSING REMARKS	Regency Ballroom
5:30-7:30 PM	MICROELECTRONICS COMMONS WELCOME RECEPTION	Regency Ballroom

Must have purchased an all-inclusive or separate ticket to attend.

