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Unlocking Opportunities:

The 2025 NSTC Workforce Awards & Top Workforce Solutions Programs

July 9, 2025

U.S. NATIONAL SEMICONDUCTOR TECHNOLOGY CENTER

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Today's Speaker

Lexie Waugh

Director, Grants & Recognition Programs

NSTC Workforce Center of Excellence

Natcast

Agenda

1. Introduction and Context Setting
   - Understanding the NSTC and the Workforce Center of Excellence
   - Key elements of award and recognition programs

2. 2025 NSTC Workforce Awards Program
   - Purpose and goals, eligibility, timeline and submission form process, questions

3. 2025 Top Workforce Solutions Program
   - Purpose and goals, eligibility, timeline and submission form process, questions

4. Questions and Answers
   - FAQ process

5. Wrap-up
   - Timeline reminders and next steps

Context Setting

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NSTC Strategic Goals

1. **Extend U.S. Semiconductor Leadership**

2. **Reduce time and cost to prototype**

3. **Build a robust workforce ecosystem**

The NSTC Workforce Center of Excellence

The NSTC Workforce Center of Excellence (WCoE) is the dedicated anchor institution that brings stakeholders together to better understand the nature of, and find solutions to, the workforce challenges facing the US semiconductor industry.

Job Categories of Focus

Engineers

Researchers and scientists

Skilled technical workers, including technicians

- Leads collaborative efforts to **increase industry access to talent**

- Leads collaborative efforts to **increase access to opportunities for individuals** in or joining the industry

Our Programmatic Capacities

Workforce Center of Excellence

NSTC Workforce Center of Excellence (WCoE)

Signals Program
Research and Data
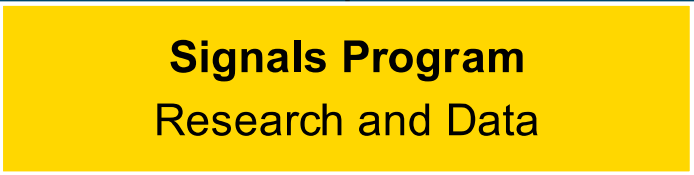
Harnessing the power of data to **discover best practices, share insights, and monitor progress**.

Workforce supply and demand data

Research and publications

Digital repository of resources and tools

Amplifier Program
Recognition and Awards

Scaling effective workforce development practices through funding and recognizing best practices.

Workforce awards

Recognition of high-quality workforce solutions

Connections Program
Workforce Services
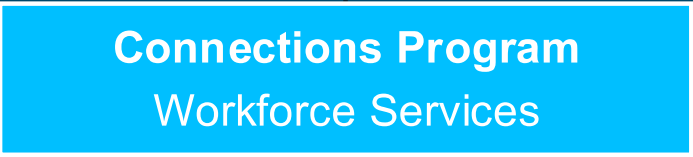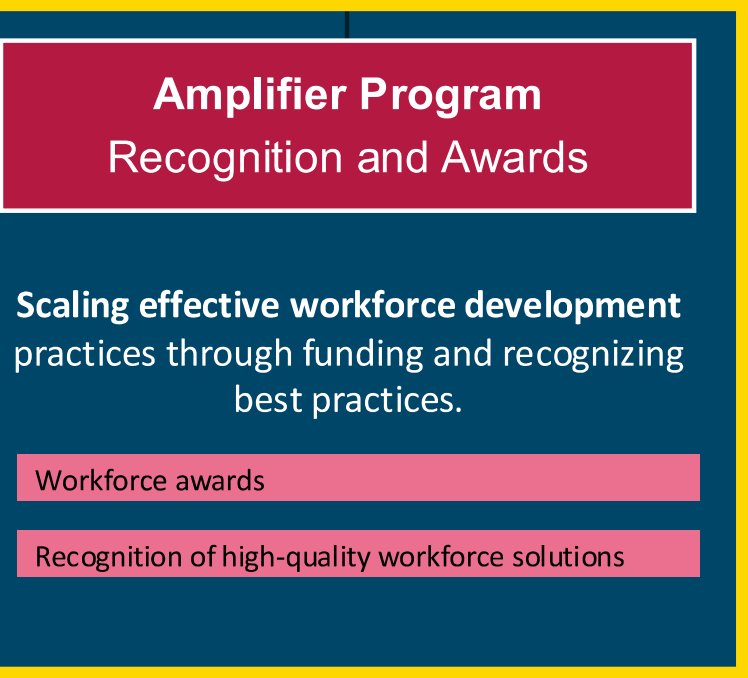
Equipping members with the tools and guidance they need to overcome challenges and grow the workforce.

Customized member services

Convenings and events

Communities of practice

Representative activities

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Introducing 2025 Award and Recognition Programs

Key Elements

Workforce Awards Program

- Supports programs addressing the critical needs within the semiconductor industry in the U.S.

- Includes a focus on scaling, expanding, realigning, and creating new programs to meet industry needs

- $500,000 - $2 million anticipated award range

- 10-20 awards anticipated

- NSTC Membership is required at the time of award

Top Workforce Solutions Program

- Increases visibility of successful program practices via recognition

- 2025 program is focused specifically on technician training programs

- Non-monetary recognition via digital badge

- NSTC Membership is not required to participate in the program
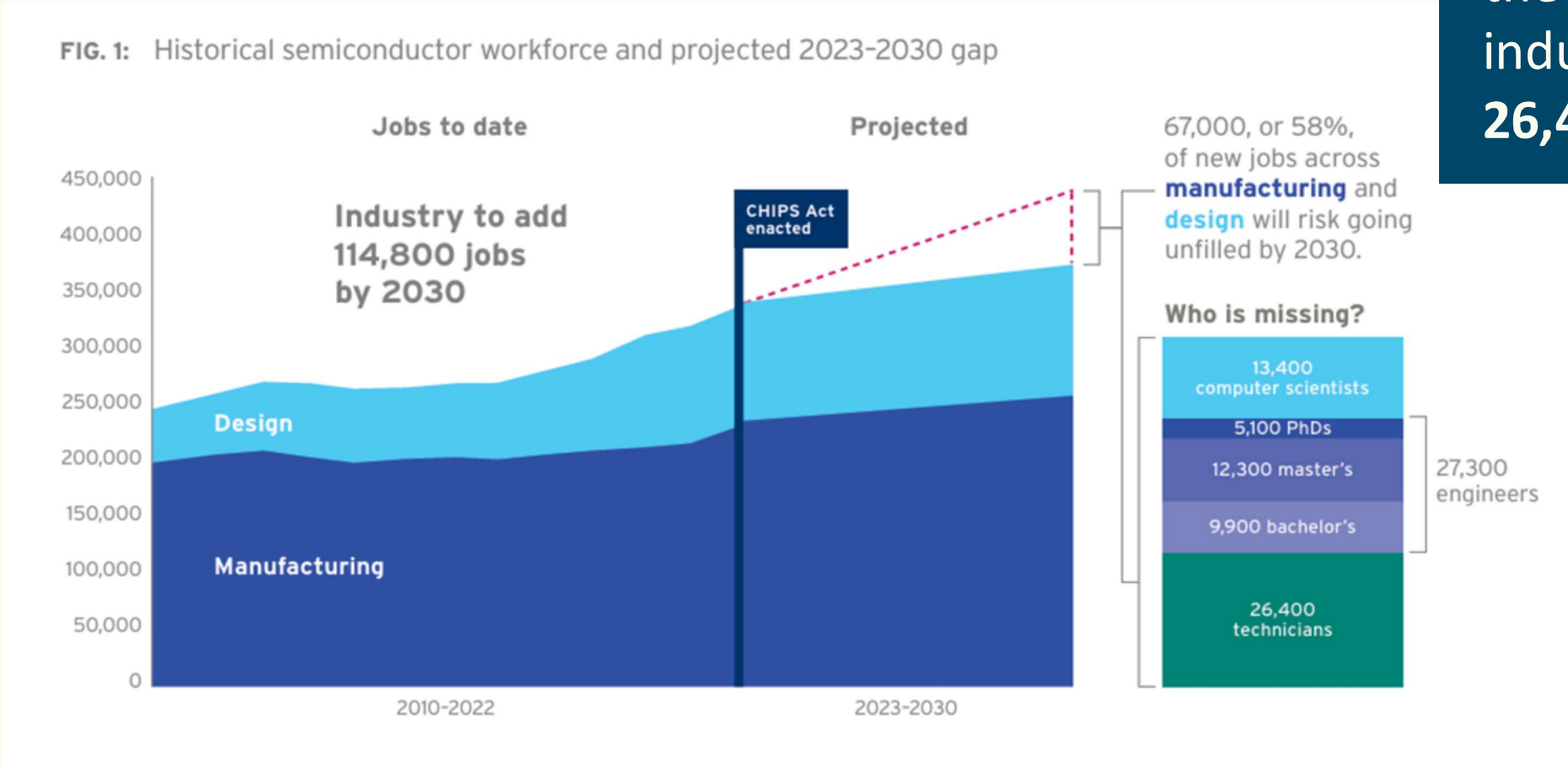
2025 NSTC Workforce Awards Program

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10

Purpose and Goals
2025 Workforce Awards Program

By 2030, 67,000 jobs risk going unfilled across the U.S. semiconductor industry. **This includes 26,400 technicians.**

FIG. 1: Historical semiconductor workforce and projected 2023-2030 gap

Jobs to date | Projected

Industry to add 114,800 jobs by 2030

CHIPS Act enacted

67,000, or 58%, of new jobs across **manufacturing** and **design** will risk going unfilled by 2030.

Who is missing?

13,400 computer scientists

5,100 PhDs
12,300 master's
9,900 bachelor's

27,300 engineers

26,400 technicians

Design

Manufacturing

450,000
400,000
350,000
300,000
250,000
200,000
150,000
100,000
50,000
0

2010-2022 | 2023-2030

General Eligibility

2025 Workforce Awards Program

Eligible applicants include domestic for-profit organizations, non-profit organizations, and accredited institutions of higher education.

NSTC Membership is not required at the time of proposal submission, but applicants must become NSTC Members at the time of award.

Individuals and unincorporated sole proprietors are <u>not</u> eligible to receive funding.

Applications may include **subawardees or contractors.** The award requires a lead applicant to serve as the administrative and fiscal agent. All subawardees must be listed in the application.

See the Call for Proposals for additional eligibility information and ineligible uses of funds.

Application Timeline

2025 Workforce Awards Program
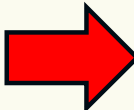
Milestone	Target Date
Application Opens	June 12, 2025
Informational Webinar	July 9, 2025
Question Submission Deadline	July 10, 2025
Intent Form Submission Deadline	**August 11, 2025**
Eligible Applicants Invite to Submit Full Proposals	September 2, 2025
Full Proposal Submission Deadline (Invite-only)	October 15, 2025
Award Notifications	Winter 2025-26

Submission Intent Form Process

Eligibility

Have you secured the permission of the lead applicant organization from appropriately authorized parties to submit this application on their behalf? *

○ Yes

○ No

Does the applicant organization have a U.S. presence? *

○ Yes

○ No

Lead Applicant Organization Unique Entity Identifier (UEI) *

Applicants must provide their UEI (formerly DUNS) number at the time of submission. For applicants who have not previously received a DUNS/UEI, applicants are asked to register with Sam.gov to request a UEI number. Applicants must be able to demonstrate they have completed the initial registration process to secure a UEI.

○ Yes, we have a UEI number.

○ No, we do NOT have a UEI number, but we have completed the initial registration process to obtain a UEI number.

○ No, we do NOT have a UEI number and we have NOT completed the intial registration process.

Does the proposed project specifically support individuals pursuing semiconductor industry related education? *

○ Yes

○ No

Proposals must incorporate a strategy to leverage additional funding. Is at least 10% of the expected project costs in this proposal covered by an external source or the lead applicant organization? *

○ Yes

○ No

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NSTC Membership

While entities are NOT required to be NSTC Members at the time of proposal submission, all awardees and subawardees who are not NSTC Members will be required to undergo vetting per the NSTC Membership process and enter into an NSTC Membership agreement prior to execution of an award agreement.

Additional information regarding NSTC Membership eligibility and the process of becoming an NSTC Member is available at natcast.org/nstcmembership.

Is the lead applicant organization an NSTC Member? *

○ Yes

⦿ No

Does the lead applicant organization attest to become an NSTC Member by the time of award agreement execution? *

○ Yes

○ No

Prev   Save   Save and Next

Lead Applicant Organization

Organization Legal Name *

Organization Name (if different)

Employer/Taxpayer Identification Number (EIN/TIN) *

Organization Address *

Street Address

Line 2

Line 3

City

Country

United States

State / Province

Select

Zip / Postal Code

Organization Type *

Select all that apply.

☐ Community college
☐ U.S. accredited institution of higher education
☐ U.S. K-12 educational institution
☐ U.S. for-profit organization
☐ U.S. non-profit organization
☐ U.S. semiconductor manufacturing company
☐ U.S. semiconductor design company
☐ U.S. semiconductor research company
☐ U.S. technology consortium, trade association or industry association
☐ Federally funded research and development center (FFRDC)
☐ Foreign entity
☐ Foreign organization
☐ Military services training institution
☐ Organized labor or union organization
☐ Other semiconductor-related company
☐ Private college
☐ Private university
☐ Technical college
☐ Training provider
☐ U.S. federal government entity
☐ U.S. Indian tribal government entity
☐ U.S. local or municipal government entity
☐ U.S. state, local, territorial, or Indian tribal government entity
☐ U.S. territory government entity
☐ Other

Organization Website *

Prev   Save   Save and Next

Authorized Representative

The individual to contact regarding this application.

Prefix *

Select ⌄

First Name *   **Middle Name**   **Last Name** *

Suffix

Job Title *   **Department/Division Name**

Organization Name *

Email Address *   **Phone** *

🇺🇸 ▾ (201) 555-0123

Prev   Save   Save and Next

Proposal Overview

Proposed Award Amount *

Amount must be between $500,000 to $2,000,000.

[   ]

Project Title *

Provide a brief, descriptive title of the project.

[   ]

Performance Period Start Date *

Applicants must specify a performance period between one to two years, starting between March 1 and May 31, 2026.

[   ]

Performance Period End Date *

[   ]

Project Summary *

Please provide a brief project summary. Include a description of the project's activities and how this project improves training and access to jobs or skills in semiconductor research, manufacturing, design, and/or production. Applicants should draft this summary in a manner that can be shared in public announcements, if awarded.

Geographic Areas Affected by the Project *

Please describe where this proposed project will be implemented and/or which geographic areas will be affected by the project (e.g., municipalities, counties, states, etc.).

Target Workforce Segment *

Select all that apply.

- [ ] Technicians
- [ ] Engineers
- [ ] Researchers
- [ ] Administrative Roles
- [ ] Data Scientist
- [ ] Other

Program Activities *

Select all that apply.

- [ ] Create new curriculum
- [ ] Develop existing curriculum
- [ ] Support paid work-based learning, including registered apprenticeship and pre-apprenticeship programs
- [ ] Issue industry-recognized credentials
- [ ] Develop credential standards
- [ ] Confer semiconductor industry-relevant degrees or support related coursework, such as undergraduate or graduate programs or coursework in computer engineering, electrical engineering, technology, informatics, computer programming, chemical engineering, or industrial engineering, offered by two- or four-year colleges or universities
- [ ] On-the-job training
- [ ] Mentorship
- [ ] Wage gains for demonstrated skills milestones
- [ ] Wraparound supports that reduce barriers to entry in program participation (e.g. childcare or transportation)
- [ ] Experiential learning opportunities such as worker cooperatives, externships, internships, fellowships, or capstone projects
- [ ] Recruitment effort
- [ ] Public awareness campaign
- [ ] K-12 program

Program Maturity Level *

Select one.

Maturity Level	Description
Novel Innovator or Designer	A new program design, concept, or limited pilot. Program lacks significant history, if any, but shows promising signs of benefit based on stakeholder input, regional conditions, or other criteria.
Early Market Incubator or Developer	Existing program that operates on a small scale such as a single employer and single training provider with a small number of participants per year. The program has limited history, but some evidence of stakeholder engagement including employers and participants.
Late Market Leader and Implementor	Existing program that operates at a moderate level of impact. Program has a few years of history, a selection or network of stakeholders, and a track record of participant completions.
Market Leader or Accelerator	Program has a high degree of past success. Program likely has existing stakeholder network such as major employer investment, strong advisory board, formalized agreements, and significant track record of participant completion and job placement.

○ Novel Innovator of Designer

○ Early Market Incubator or Developer

○ Late Market Leader and Implementor

○ Market Leader or Accelerator

Does this project involve subawardees? *

For the purposes of this section, a subawardee is an entity that carries out a defined, substantive portion of the programmatic work. For example, a subawardee may help design the program execution framework, determine eligibility of program participants, or develop publishable results in relation to the program. Subawardee personnel may be included as PI/PD, Co-PI or key personnel on the award, and performance is measured against whether the award objectives are met. A subawardee has responsibility for adherence to applicable Federal program compliance requirements.

○ Yes

○ No

Ecosystem Partners *

Select the options below that best describe the types of entities that will be active partners in this project.

Select all that apply.

- ☐ Career/Technical Education provider(s)
- ☐ Community Based Organizations
- ☐ Community College(s)
- ☐ Economic Development
- ☐ Employer(s)
- ☐ Higher Education
- ☐ K-12 Education
- ☐ Labor Organization(s)
- ☐ Non-profit(s)
- ☐ Sector Partnership Organization(s)
- ☐ State and/or Local Government
- ☐ State and/or Local Workforce Development Board(s)
- ☐ Regional Workforce Intermediaries
- ☐ Other

Leveraged Funding Strategy *

Proposals must incorporate a strategy to leverage 10% of the total funding from external stakeholders or the applicant organization. Please provide a brief description of the leveraged funding strategy.

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Prev   Save   Save and Next

NSTC | Natcast 22

Project Design

Project Design *

Describe the project, including its theory of change. Your theory of change should describe relevant inputs, outputs, and short-, medium-, and long-term outcomes. Include the type of project or design and its attributes, such as recruitment strategies, program duration, skills to be acquired, credentials awarded and whether they are stackable and aligned with a career pathway. Include any prior successes using these methods, describe the outcomes and lessons learned, and explain how goals will be met. Include evidence-based strategies, partners and stakeholders, and maturity of project and partnerships.

Word count: 0 / 800

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Indicate the ways in which this award will directly enable project implementation and/or scaling and expansion. *

Word count: 0 / 250

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Anticipated Outcomes

Please enter your anticipated outcomes for the project.

Total number of participants recruited *

[   ]

Total number enrolled in the program *

[   ]

Total number of participants completing the program *

[   ]

Total number of program completers to enter the semiconductor industry *

[   ]

Prev | Save | Save and Next

Employer Letter

Please upload at least one Letter of Support or Commitment from a private sector semiconductor industry employer. If the lead applicant is an employer, please outline your commitments in writing following the guidelines below.

For the purposes of this section, a semiconductor industry employer is defined as a private sector company that is directly involved in the design, manufacturing, or production of microelectronics or equipment used to produce microelectronic components. It does NOT include other members of the semiconductor workforce ecosystem, such as training providers, academic institutions, consulting firms, non-profit organizations, or investment firms.

In the review of full proposals, preference will be given to letters from NSTC members in good standing. A full list of current NSTC members is available at natcast.org/nstcmembership/members.

Letters must:

1. *Provide a description of the proposed relationship between the applicant and employer as it relates to the execution of the proposed project.*
2. *Address any numerical training/interview/hiring commitments from the employer.*
3. *Provide a description of how successful implementation of this project would positively affect the employer's workforce.*
4. *Address any committed funding support.*
5. *Be signed by an individual with authority to legally bind the organization to its commitment.*

Employer Letter Upload *

Combine all letters into one PDF. Each letter must not exceed one page.

Accepted file types: pdf
File may not exceed 5 MB in size.

| Choose File | No file chosen |

Employer Letter Information *

Provide the following information for each uploaded letter.

Letter Details	Action
No Letter Details have been added. Click on Add Letter Details below.	

Add Letter Details

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Acknowledgement of Terms

Acknowledgement of Terms *

1. You have read the full Call for Proposals (CFP) solicitation associated with this application, understand, and consent to the terms listed therein.
2. The lead applicant has a U.S. presence.
3. The lead applicant is not a foreign entity of concern or foreign country of concern, as those terms are defined in 15 C.F.R. 231.104 and 231.102, respectively.
4. The lead applicant and subawardees proposed in this program are not banned from doing business with the U.S. government. They are not debarred, suspended, or otherwise excluded from receiving an award by the Federal Government or otherwise prohibited by Presidential Executive Order or law from receiving an award from the Federal Government as listed in Sam.gov.
5. No Federal appropriated funds have been paid or will be paid to any person for influencing or attempting to influence an officer or employee of any agency, a Member of congress, an office, or employee of Congress, or an employee of a Member of Congress on the lead applicant's or subawardee's behalf in connection with the making of an award under this CFP.
6. Awards will be required to undergo vetting per the NSTC membership process and enter into a membership agreement with NSTC at the time of award.
7. No funds proposed in this application will be used for construction costs, profits, product development, commercialization, or the acquisition or lease of real property.
8. This application may be shared with Federal agencies and any parts of a proposal shared with Federal agencies may be subject to requests under The Freedom of Information Act (5 U.S.C. § 552).

By signing this Submission Intent Form, I represent and warrant that the statements herein are true, complete and accurate to the best of my knowledge. I also provide the above assurances; and agree to comply with any resulting terms if I accept an award. I am aware that any false statements or misrepresentations may result in the rejection of the application or the termination of any grant awarded.

☐ I Agree

Prev   Save   Save and Submit

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26

Full Application Process

Full Application Process

2025 Workforce Awards Program

Applicants selected to submit a full proposal will be asked for additional information, including:

- Project goals and objectives

- Statement of need

- Project workplan

- Scaling and sustainability plan

- Stakeholder engagement strategies

- Staffing plan

- Finance and budgetary information

- Additional letters of support

- Creation of shared resources for NSTConnect

QUESTIONS

2025 Workforce Awards Program

2025 NSTC Top Workforce Solutions Program

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Purpose and Goals

2025 Top Workforce Solutions Program

By evaluating and acknowledging highly effective programs, the NSTC Top Workforce Solutions program will contribute to the continued development of a skilled semiconductor workforce in the U.S.

Natcast will provide recognized programs with a **digital badge** that can be displayed online and in printed materials. Recognized programs will also be celebrated:

- In a **press release**
- On the **Natcast website**
- In NSTC and WCoE **resources and publications**

Recognized programs may enjoy additional benefits, including:

- **Boosted credibility**: Demonstrated commitment to excellence and effectiveness.
- **National visibility:** Increased visibility of programs to a broad, national audience.
- **Partnership opportunities**: Enhanced standing with industry employers, prospective funders, prospective partners, and other ecosystem stakeholders.
- **Increased student appeal**: Increased appeal of the program to those seeking reputable educational opportunities.

General Eligibility

2025 Top Workforce Solutions Program

Eligible applicants include domestic for-profit and non-profit organizations, including institutions of higher education, career and technical schools, workforce development organizations, regional training centers, and other providers of relevant technician training.

Eligible applicants must be a provider of training that results in a **certification or credential offered by a community college or a technical training provider**. Individuals, unincorporated sole proprietors, and foreign entities of concern are not eligible.

See the website for additional eligibility information and ineligible uses of funds.

Programs must:

- prepare participants for careers as technicians in the domestic semiconductor industry or closely related fields, resulting in the issuance of a relevant, industry-recognized certificate to participants.

- demonstrate that the participants are qualified to directly enter the semiconductor workforce upon completion.

- focus on participants aged 16 and older.

- be provided in the United States.

- be accredited by a recognized agency (if the program typically requires accreditation)

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Application Timeline

2025 Top Workforce Solutions Program
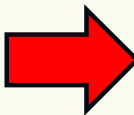
Milestone	Target Date
Application Opens	June 26, 2025
Informational Webinar	July 9, 2025
Application Closes	**August 25, 2025**
Recognition Notification	Winter 2025-26

Application Process

Eligibility

Is the applicant organization a provider of training that results in a certification, degree, or other credential? *

○ Yes

○ No

Does the applicant organization have a U.S. presence? *

○ Yes

○ No

Although this program does not involve the distribution of federal funds to recognized programs, entities that are debarred, suspended or otherwise prohibited from receiving federal funds will not be eligible for recognition. Is the applicant organization debarred, suspended or otherwise prohibited from receiving federal funds? *

○ Yes

○ No

Prev   Save   Save and Next

Organization Information

Organization Legal Name *

[   ]

Organization Name (if different)

[   ]

Employer/Taxpayer Identification Number (EIN/TIN) *

[   ]

Organization Address *

Street Address

[   ]

Line 2

[   ]

City

[   ]

Country

[United States   ⌄]

State / Province

[Select   ⌄]

Zip / Postal Code

[   ]

Organization Type *

Select all that apply.

☐ Community college
☐ Federally funded research and development center (FFRDC)
☐ Foreign entity
☐ Foreign organization
☐ Military services training institution
☐ Organized labor or union organization
☐ Private college
☐ Private university
☐ Technical college
☐ Training provider
☐ U.S. accredited institution of higher education
☐ U.S. federal government entity
☐ U.S. for-profit organization
☐ U.S. Indian tribal government entity
☐ U.S. K-12 educational institution
☐ U.S. local or municipal government entity
☐ U.S. non-profit organization
☐ U.S. semiconductor design company
☐ U.S. semiconductor manufacturing company
☐ U.S. semiconductor research company
☐ U.S. semiconductor company (other)
☐ U.S. state, local, territorial, or Indian tribal government entity
☐ U.S. technology consortium, trade association or industry association
☐ U.S. territory government entity
☐ Other

Point of Contact

The individual to contact regarding this application.

Prefix

Select ⌄

First Name *   **Middle Name**   **Last Name** *

Suffix

Job Title *

Department/Division Name   **Organization Name** *

Email Address *   **Phone** *

🇺🇸 ▾ (201) 555-0123

Prev   Save   Save and Next

Program Overview

Program Name *

Provide a brief, descriptive title of the program.

[   ]

Program Summary *

Provide a brief overview of the program, including its objectives, target audience, and key activities. Applicants should draft this summary in a manner that can be shared in public announcements, if selected.

Word count: 0 / 500

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Cohort Size

For the purposes of this application, a cohort is defined as the number of unique individuals who are trained or educated at the same time within the same program.

What is the typical cohort size? Provide a range and average as # of participants.

 # Participants Range from: *   **# Participants Range to:** *

[   ]   [   ]

Average # of Participants: *

[   ]

Geographic Areas Affected by the Program *

Describe where this program is implemented and/or which geographic areas are affected by the program (e.g., municipalities, counties, states, etc.).

Program Duration *

How long does it take for one cohort to complete the program? Specify the duration in years, months, weeks, days, or hours, depending on the program. Provide context if needed. Additionally, include the start and end dates for the most recent cohort as an example.

Word count: 0 / 250

Target Workforce Segment *

Select all that apply.

- ☐ Technicians
- ☐ Engineers
- ☐ Researchers
- ☐ Administrative Roles
- ☐ Data Scientists
- ☐ Other

Prev   Save   Save and Next

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Program Details

Program Design and Practices *

a. Describe the program's purpose and design, including:

- Skills acquired and credentials awarded,
- How these align with specific semiconductor career pathways,
- Availability of hands-on learning opportunities,
- Availability of work-based learning opportunities, and
- Availability of supportive services for learners.

b. Lastly, describe the program's maturity and all key partners, including relevant aspects of each partnership.

Word count: 0 / 800

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Evidence-Based Strategies *

a. Describe the evidence-based strategies used to develop and implement the program. List each relevant practice and a brief description of the evidence base.

Word count: 0 / 250

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Program Outcomes *

a. Describe the makeup of a typical cohort. What types of learners are you serving (e.g. adult learners, first time in college students, etc.)?

b. Describe the program's outcomes and achievements to date, including (if applicable):

- *The average completion rate across all cohorts as well as the range of completion rates,*
- *The total number of participants who've completed the program to date, and*
- *Methods used to connect participants to long-term career paths.*

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Employment Outcomes *

a. Across all cohorts to date, what percentage of program participants have been employed at a semiconductor company within 12 months of program completion?

b. How did you determine this number?

c. How do you collect this data (e.g. state longitudinal data system, dept. of labor, self-report, etc.)?

Word count: 0 / 600

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Labor Market Demand and Employer Involvement *

a. Describe the local or regional labor market demand for the semiconductor industry. What do the next 3-5 years look like? What data are you using to assess the local or regional labor market?

b. Describe the ways you are engaging employers in the program. How are you assessing industry needs and program relevancy? How often? What roles do employers play in this process?

c. Share examples of the semiconductor companies hiring your program completers.

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Recruitment Practices *

a. Describe the program's recruitment practices. What methods are being used to recruit participants?

b. How are you measuring the success of these methods?

c. Which methods have been most successful in getting learners to sign up for and complete the program?

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Capacity *

a. Describe the program's current capacity, including the maximum number of participants per cohort.

b. What capacity factors determine enrollment? Describe any limiting factors (e.g. lab space, classroom space, etc.).

c. How has capacity changed over time? How do you expect the capacity to change within the next two years?

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Program Cost *

a. What does it cost the host institution, per head, to run this program? How is this determined?

b. How much does an individual pay to participate in the program?

c. What portion of students receive scholarships or other financial assistance to cover the costs of participation? What is the average amount of financial assistance?

Word count: 0 / 250

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Employer Feedback Survey

To complete your application, at least one program feedback survey must be submitted to Natcast directly from a semiconductor industry employer. For the purposes of this section, a semiconductor industry employer is defined as a private sector company that is directly involved in the design, manufacturing, or production of microelectronics or equipment used to produce microelectronic components.

Use the fields below to email survey requests to your employer contact/s. After you send the request, the contact will automatically receive an email with instructions to complete the survey. When the employer successfully submits the survey, you will automatically receive a notification email.

We recommend sending survey requests immediately to allow your contacts sufficient time to submit before the deadline stated below. We recommend communicating with your employer contact/s to ensure they submit the survey by the deadline. Submitted applications without at least one complete survey will be removed from consideration.

The content of survey responses will never be made visible to applicants.

Employer feedback surveys must be submitted by the application deadline Monday, August 25 at 5pm ET.

Instructions:

1. Enter the full name of the employer you wish to send the survey.
2. Enter their email address.
3. Click "Send Request Now."
4. If your request was successfully sent, you will see (Not Received). It means that the employer has yet to submit their feedback, not that your email request itself was unsuccessful.
5. Officially submit your application. Employers may continue to submit surveys after you have submitted your application. However, you may not send additional survey requests after submitting the application, so be sure to send all survey requests now.
6. After the employer submits the survey, you will receive an email notification confirming their submission. You will be able to check the status of surveys in your application portal.

Employer Feedback Survey Request #1 *

(Not Sent)

Full Name

Email

Send Request Now

Employer Feedback Survey Request #2

(Not Sent)

Full Name

Email

Send Request Now

Employer Feedback Survey Request #3

(Not Sent)

Full Name

Email

Send Request Now

Required Supporting Documentation

Upload a copy of the program's syllabus and at least one sample recruitment document. The selection team will review these to better understand the program.

Accepted file types: pdf

Each file may not exceed 5 MB in size.

Syllabus *

Upload a single PDF containing the program's syllabus.

| Choose File | No file chosen |

Recruitment Materials *

Upload a single PDF containing recruitment materials.

| Choose File | No file chosen |

Prev **Save** **Save and Next**

Optional Supporting Documentation

Please upload any relevant supporting documentation, such as program reports, testimonials, or case studies. The selection team will review these to better understand the program. Multiple fields are available to allow for multiple file types.

Accepted file types: avi, doc, docx, gif, jpeg, jpg, mp3, mp4, mpeg4, pdf, png, xls, xlsx

Each file may not exceed 5 MB in size.

Supporting Documentation #1 (Optional)

| Choose File | No file chosen |

Supporting Documentation #2 (Optional)

| Choose File | No file chosen |

Supporting Documentation #3 (Optional)

| Choose File | No file chosen |

Prev   Save   Save and Next

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Terms and Conditions

Acknowledgment of Terms *

By signing this application, I represent and warrant that:

1. I have the authority to make this submission on behalf of the applicant organization.
2. The applicant organization has a U.S. presence.
3. The applicant organization is not a foreign entity of concern or foreign country of concern, as those terms are defined in 15 C.F.R. 231.104 and 231.102, respectively.
4. I understand that this application may be shared with Federal agencies, and that any parts of a proposal shared with Federal agencies may be subject to requests under The Freedom of Information Act (5 U.S.C. § 552).
5. The statements contained in my submissions are true, complete and accurate to the best of my knowledge.

I also provide the above assurances. I am aware that any false statements or misrepresentations may result in the rejection of the application or the termination of the applicant program's recognition.

☐ I Agree

Prev **Save** **Save and Submit**

QUESTIONS

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Questions and Next Steps

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Thank You!