

Core Membership

Leading The Way

At the NSTC, we're creating the future of technology and accelerating the speed of innovation to address some of the world's most pressing challenges.

A key component of the CHIPS and Science Act, the NSTC is a public-private partnership creating an unprecedented infrastructure to accelerate R&D innovation, develop a skilled workforce, and strengthen the United States' leadership position in the global semiconductor industry.

This is a once-in-a-generation opportunity to create the technologies that transform the world, and you can be part of it.

Our Priorities

The NSTC is centered on three strategic goals

1. Extend U.S. leadership in foundational technologies for future applications and industries, and strengthen the U.S. semiconductor ecosystem
2. Significantly reduce the time and cost to prototype innovative ideas for member organizations
3. Build and sustain a robust semiconductor workforce development ecosystem

In order to chart a path forward, we must break down barriers.

The NSTC is leading the way in next-generation semiconductor technologies by investing in leading-edge research, physical and digital assets, and workforce development. These resources are aimed at reducing barriers for members to bring new technologies from lab-to-fab as well as supporting member efforts to build and sustain a strong U.S. semiconductor development ecosystem.

The NSTC is a member-driven consortium. NSTC members will have the opportunity to contribute to overall strategy, share best practices, guide the NSTC research agenda, and actively collaborate on research and workforce development objectives.

NSTC

**U.S. NATIONAL
SEMICONDUCTOR
TECHNOLOGY CENTER**

What is Core membership?

Core membership is designed for organizations which seek to engage with the full suite of NSTC offerings. Core membership will provide opportunities for collaboration among other NSTC members; access to NSTC facilities, programs, and resources; and access to workforce development best practices and initiatives through the NSTC Workforce Center of Excellence.

Join Us

Prospective members are invited to submit a membership application to initiate a formal review process.

Ready to join
our community of
innovators and
leaders?

**Visit [natcast.org/
NSTCmembership](https://natcast.org/NSTCmembership)**



The NSTC, established by the CHIPS and Science Act of the U.S. government, is operated by Natcast, a purpose-built, non-profit entity.

Who is a Core member?

NSTC Core members include:

Companies operating within the semiconductor ecosystem—spanning design, manufacturing, materials, end-users, and others	Academic institutions conducting semiconductor-related research	Research labs engaged in semiconductor research activities	Non-profit entities with an emphasis on semiconductor R&D
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What are the benefits of Core membership?

There are three pillars of NSTC membership: Educate, Collaborate, and Innovate. NSTC Core members have the opportunity to engage in the full suite of these benefits:

How much does Core membership cost?

Organizations interested in joining the NSTC will pay a base fee for Core membership determined by their entity type and size. Please visit natcast.org/NSTCMembership to learn more.

Benefits	Included	Anticipated Availability
Educate		
Access to reports, news, webinars, seminars	✓	Now
Eligibility for workforce funding opportunities	✓	Now
Access to Workforce Center of Excellence resources and programs	✓	2025
Access to workforce program experts	✓	2025
Collaborate		
Access to NSTC Conferences & events to network with other members and	✓	Now
Eligibility for Workforce Advisory Board (WFAB) to guide workforce priorities	✓	Now
Eligibility for Technical Advisory Board (TAB) to guide research agenda	✓	Now
Teaming & co-development to learn from and work with other members	✓	2025
Co-location opportunities with other members in neutral NSTC facilities*		2026
Ability to participate in technology sector communities of interest	✓	2025
Innovate		
Eligibility for research grant awards to fund R&D initiatives	✓	2025
Ability to participate in NSTC-led research projects	✓	2026
Ability to participate in member-funded research projects*		2026
Rights to use NSTC IP assets*		2026
Opportunities to present to partner investment funds to earn funding for additional projects	✓	2026
Design Enablement Gateway capabilities to reduce time and cost to design and facilitate collaboration*		2025
Silicon aggregation services to access multi-project wafers and other production needs*		2025
Facilities EUV Accelerator*		2025
Facilities (e.g front end, packaging) providing prototyping and development opportunities*		2026+

*indicates additional fee for access and usage